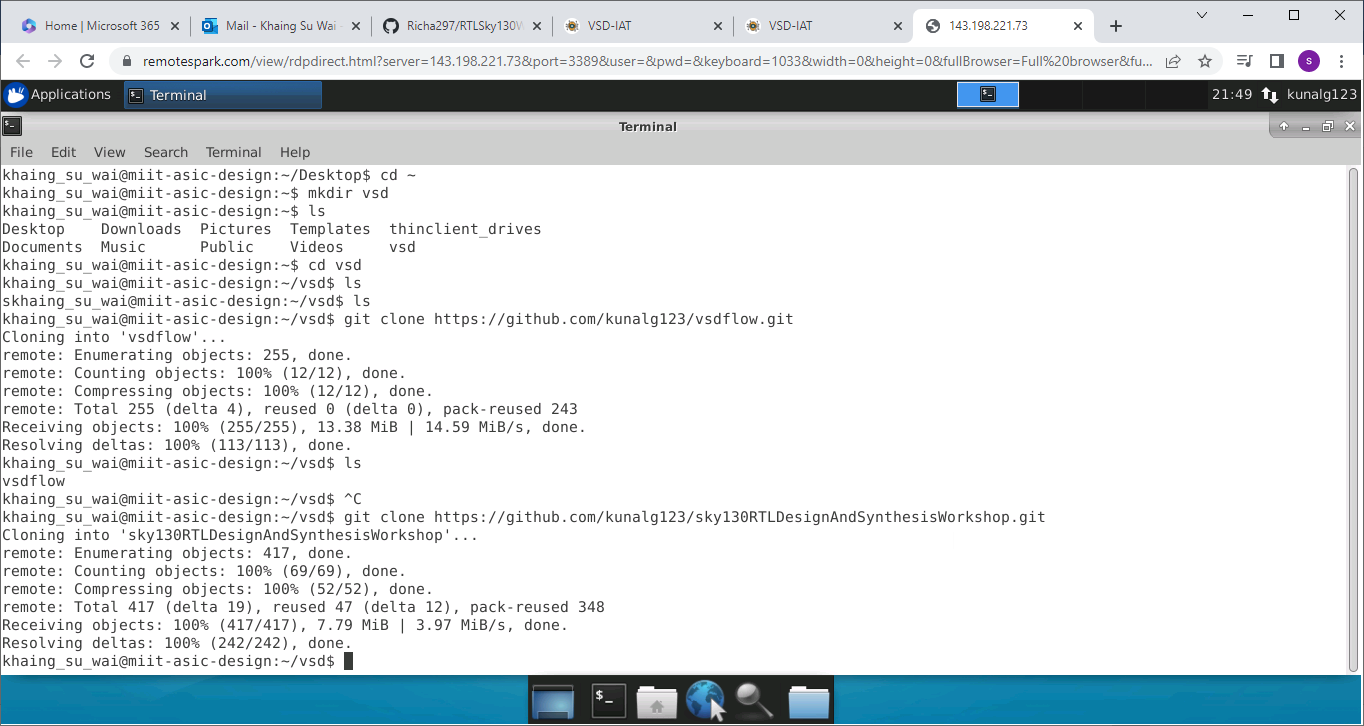
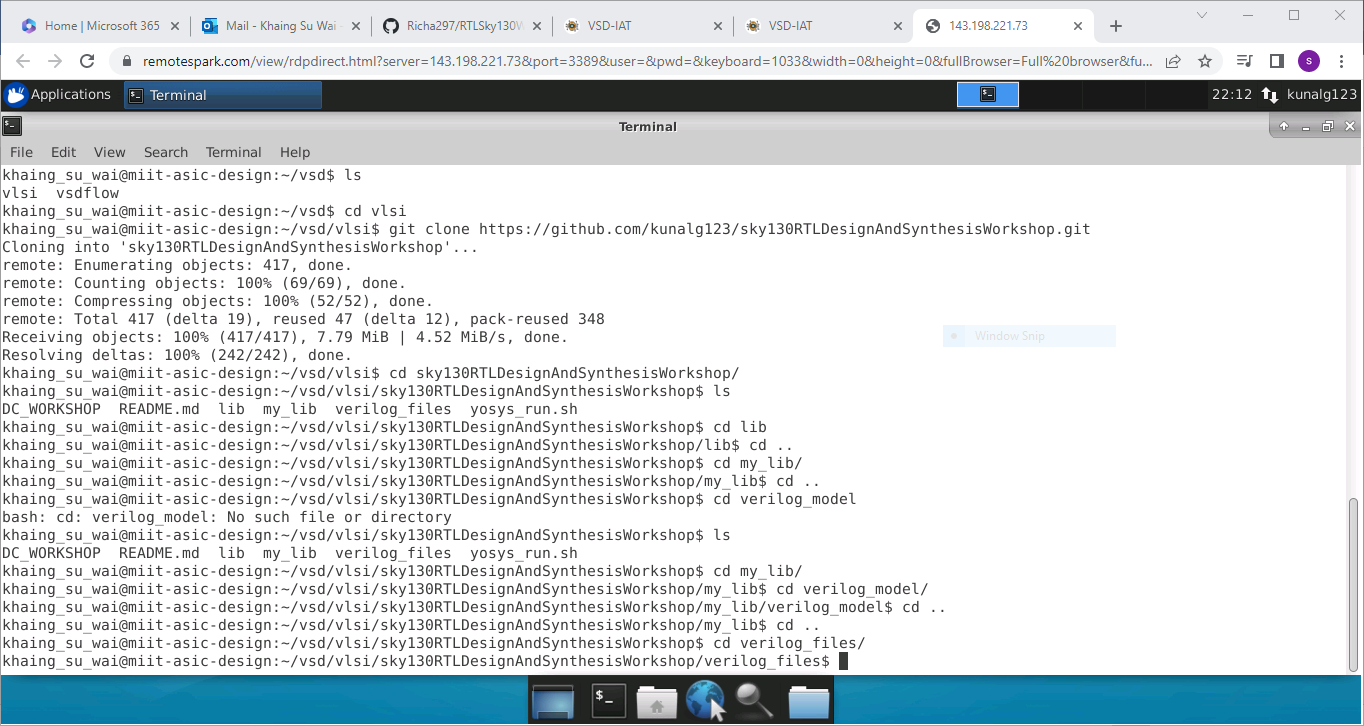
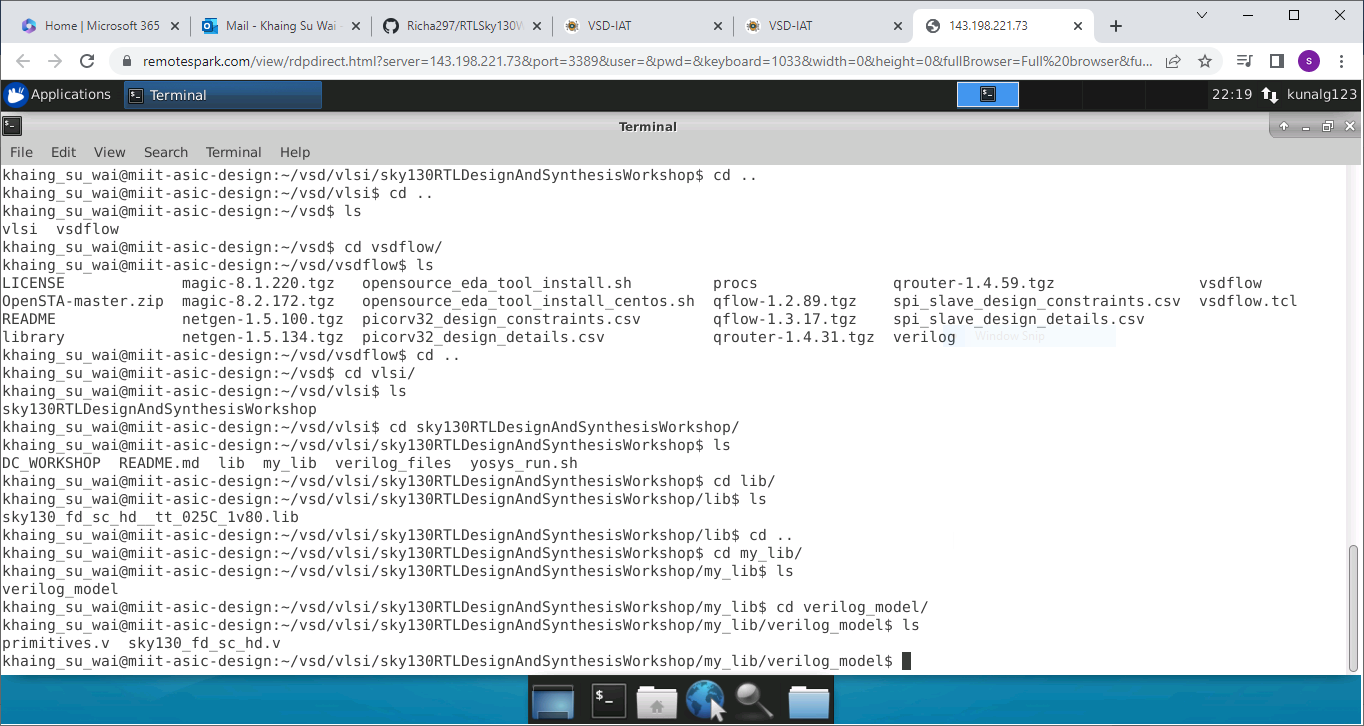
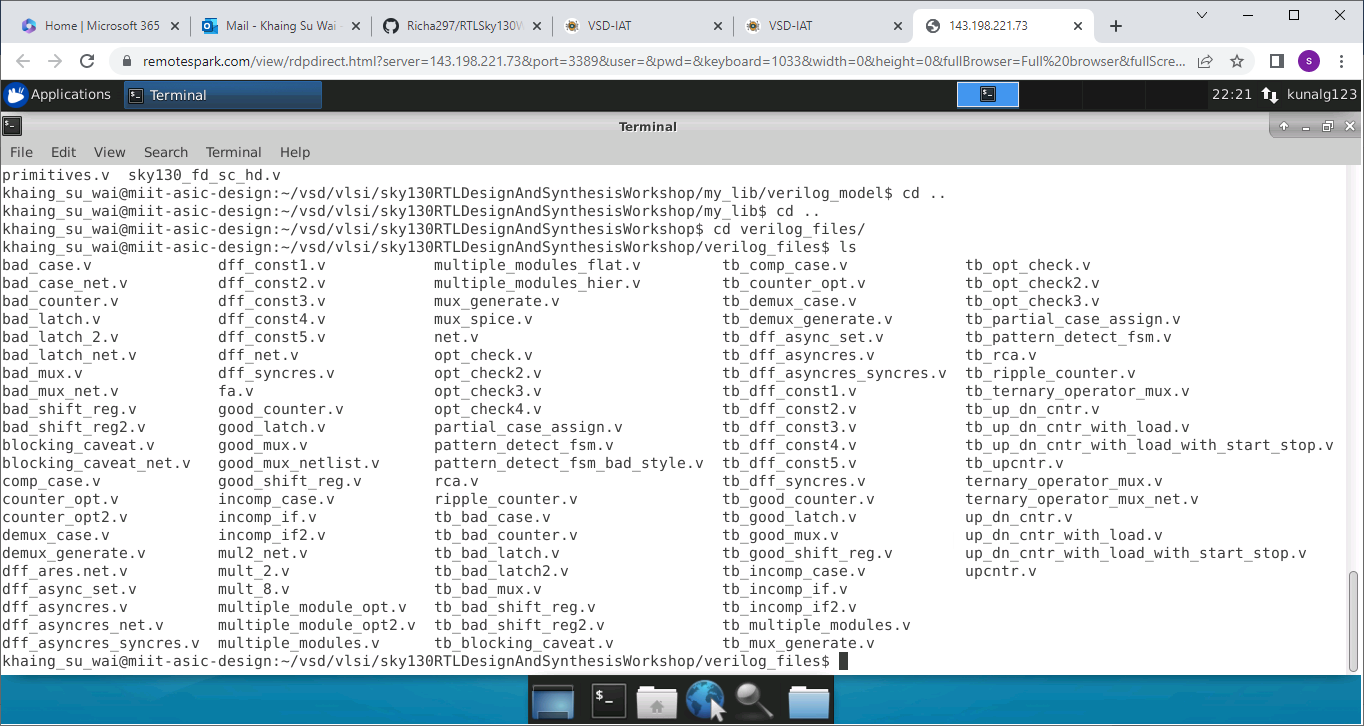
**Labs using iverilog and gtkwave**

Pic1

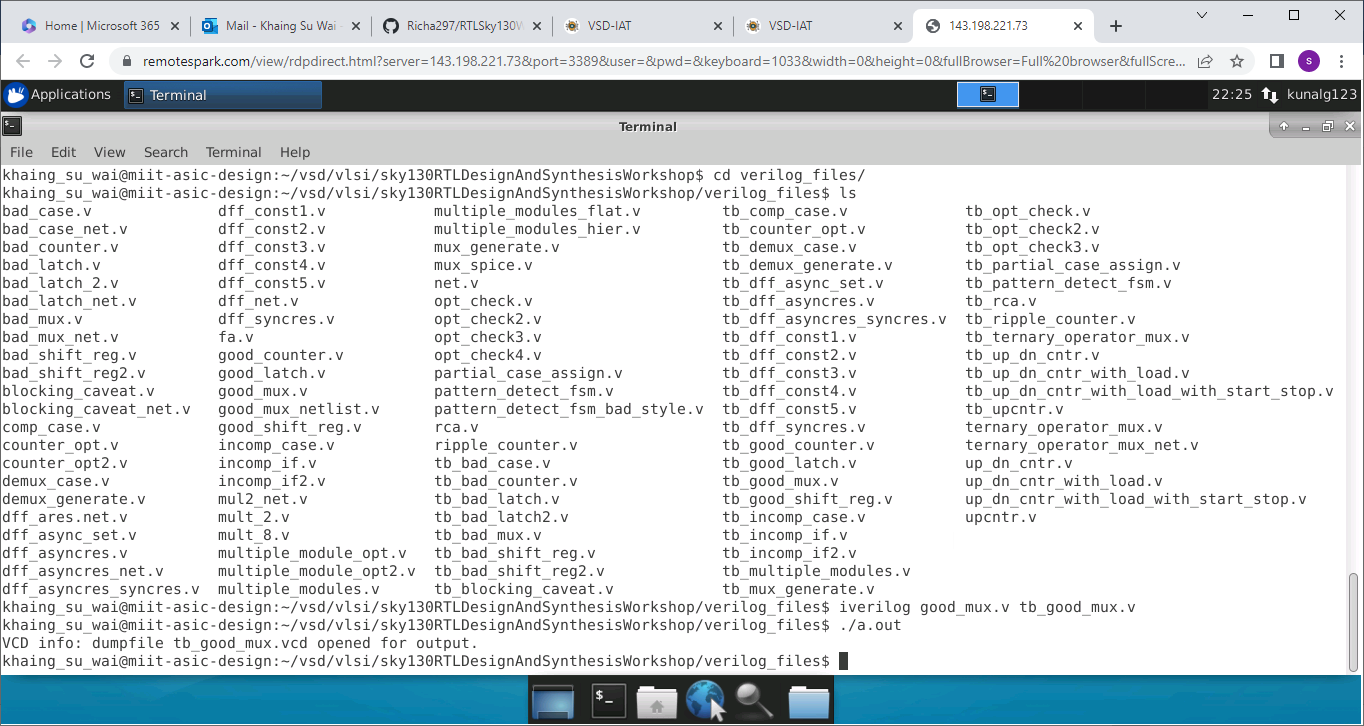
Pic2

Pic 3

Pic 4

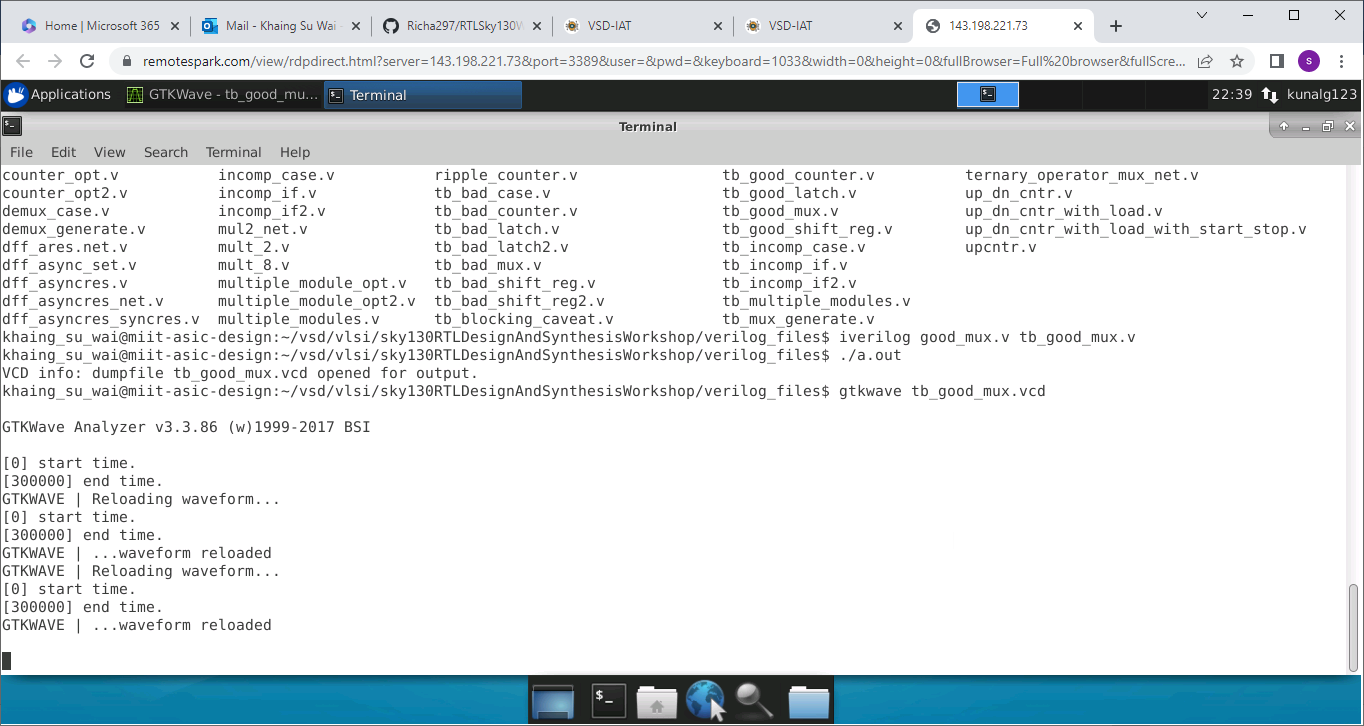


iverilog good\_mux.v tb\_good\_mux.v

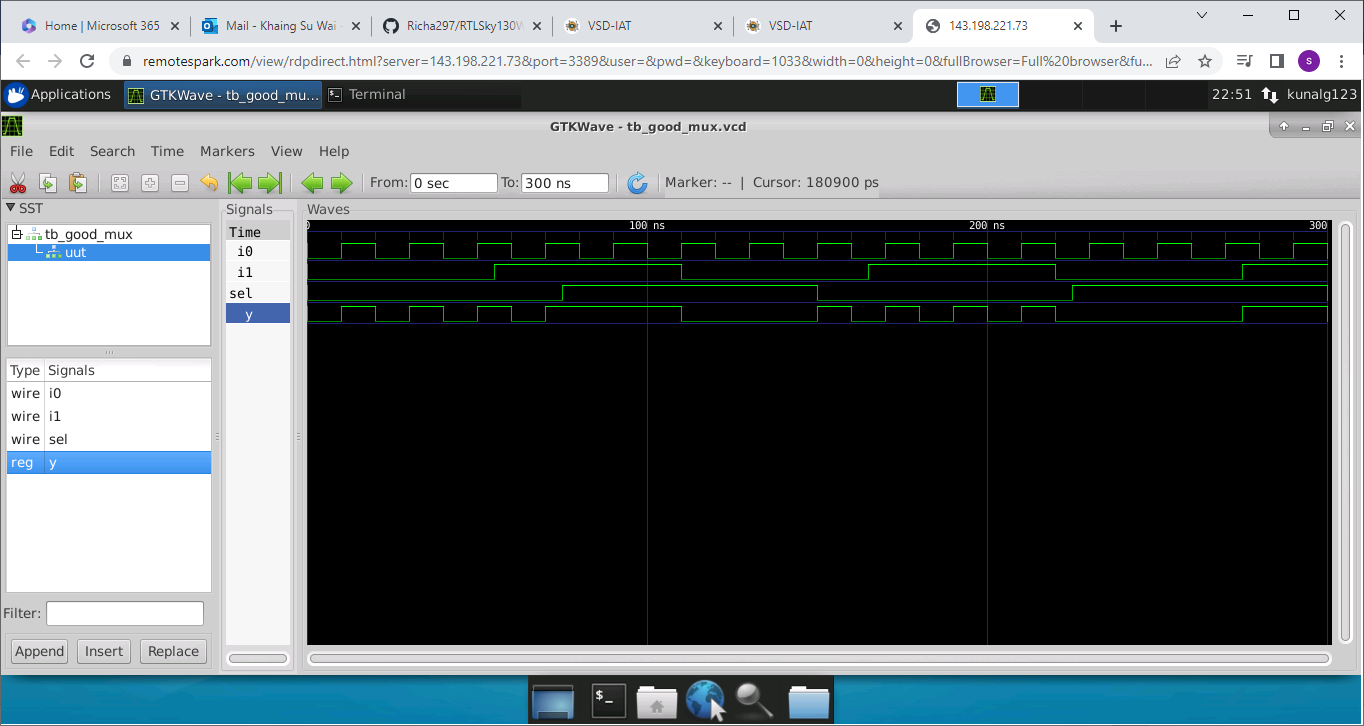
Pic 5

gtkwave tb\_good\_mux.vcd

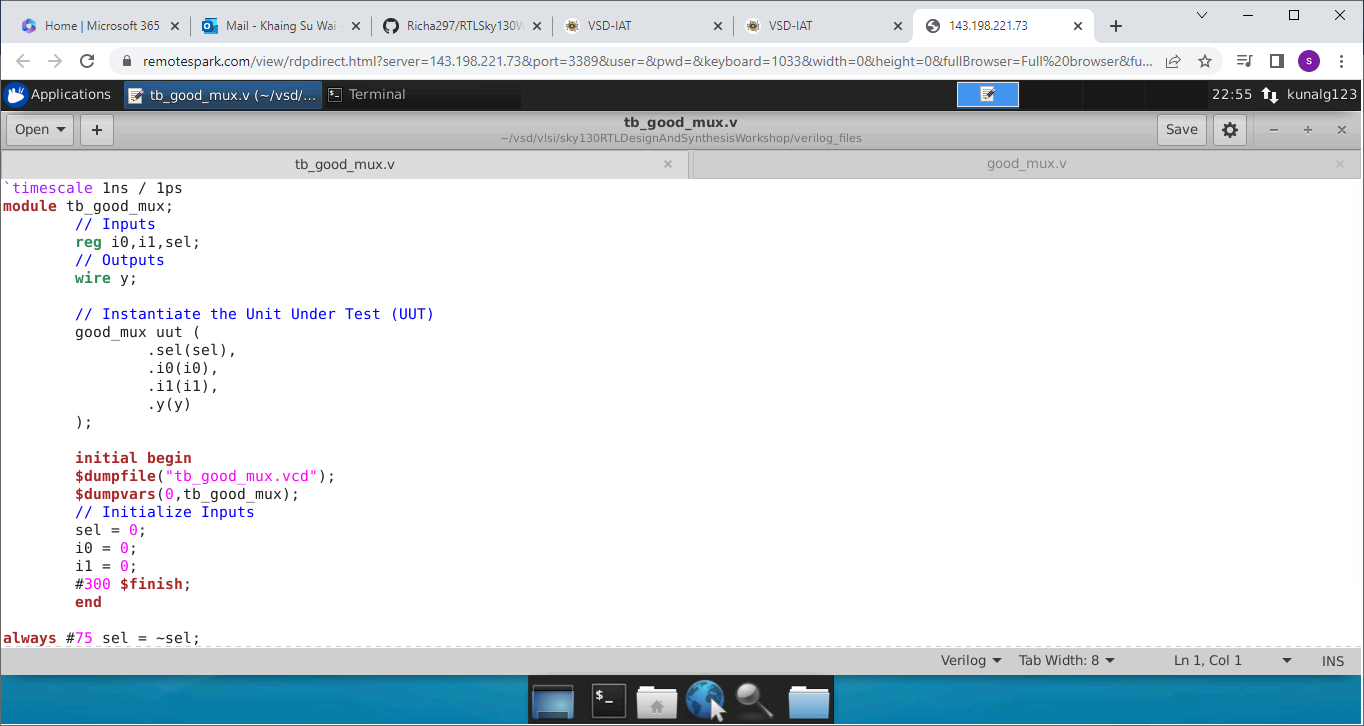
Pic 6

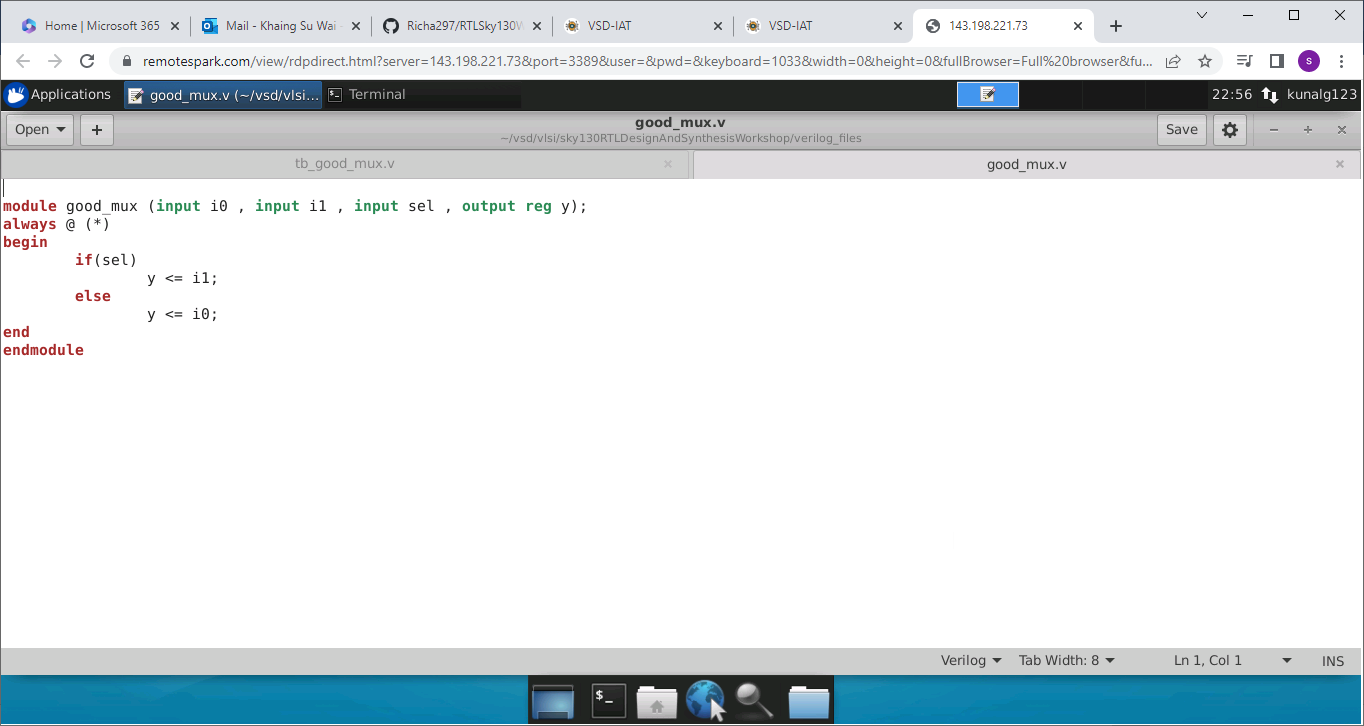


Pic 7



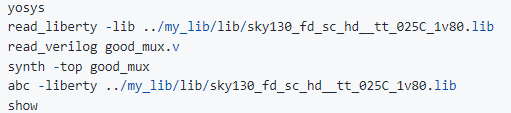
vim tb\_good\_mux.v -o good\_mux.v

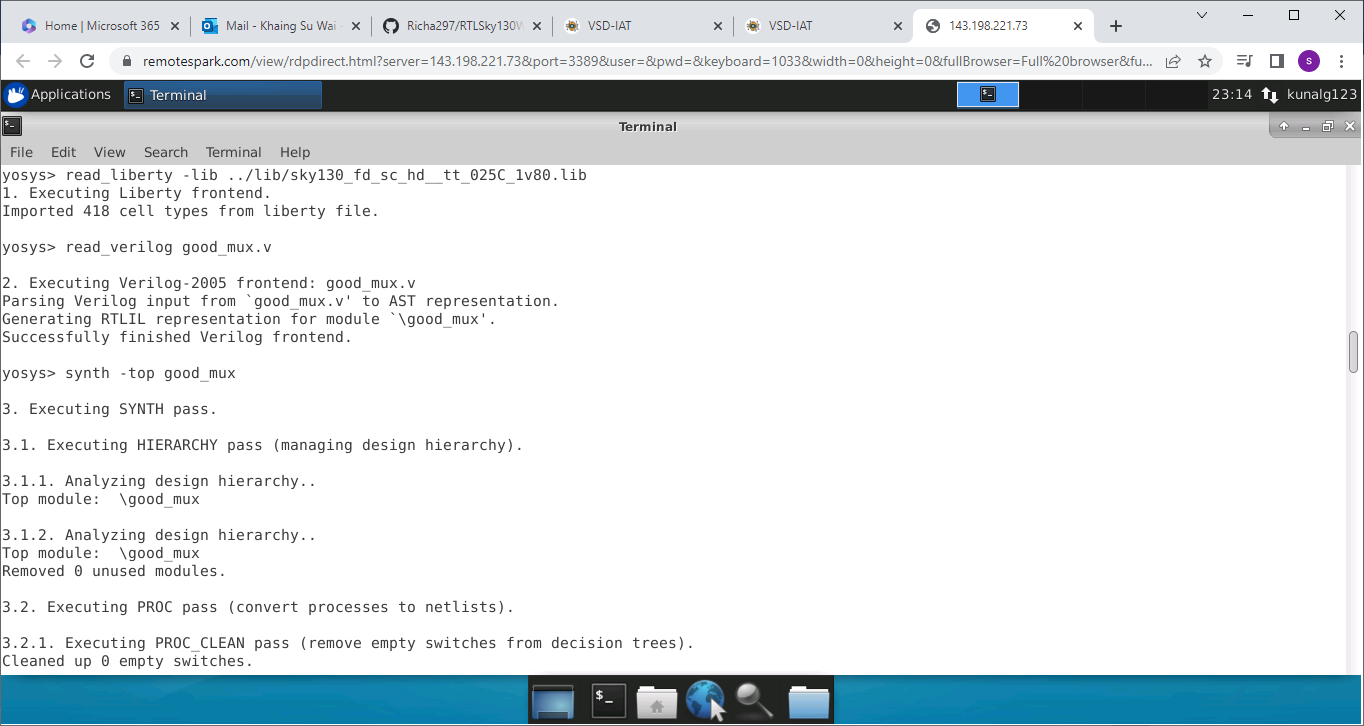
Pic 8

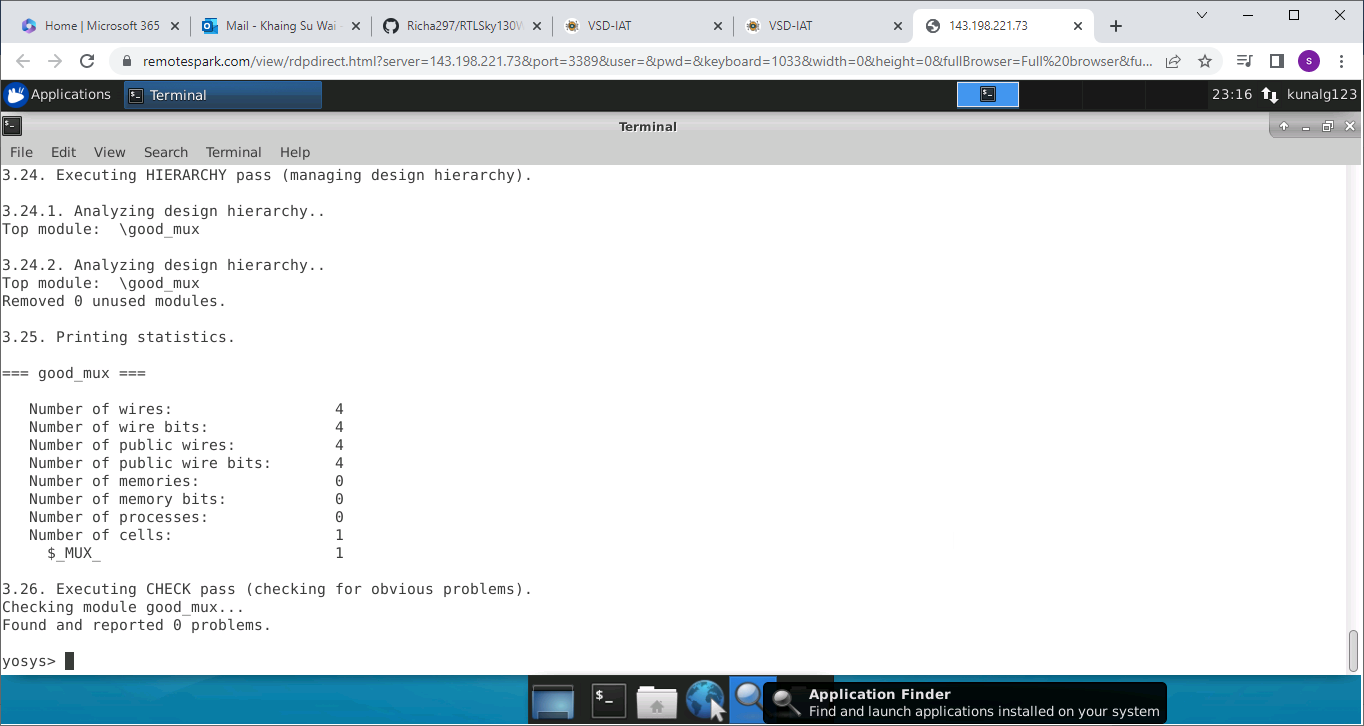
Pic 9

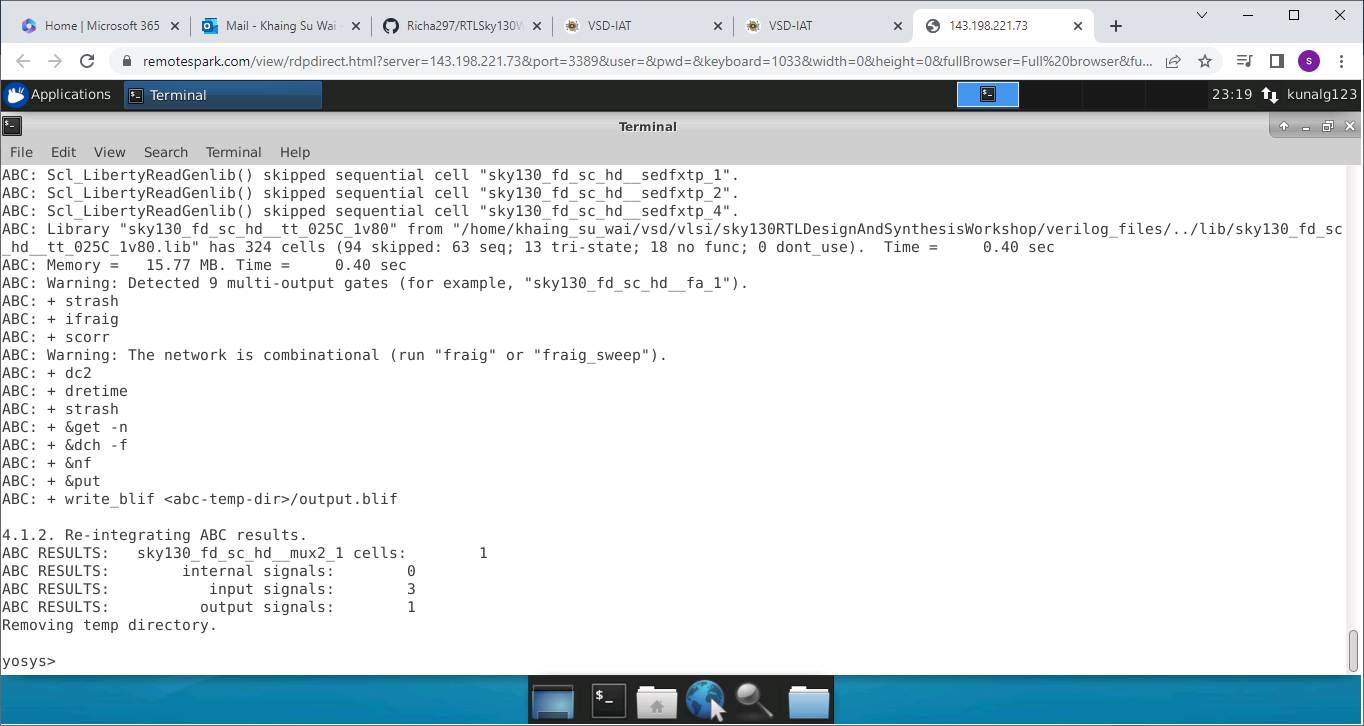
**Labs using Yosys and Sky130 PDKs**

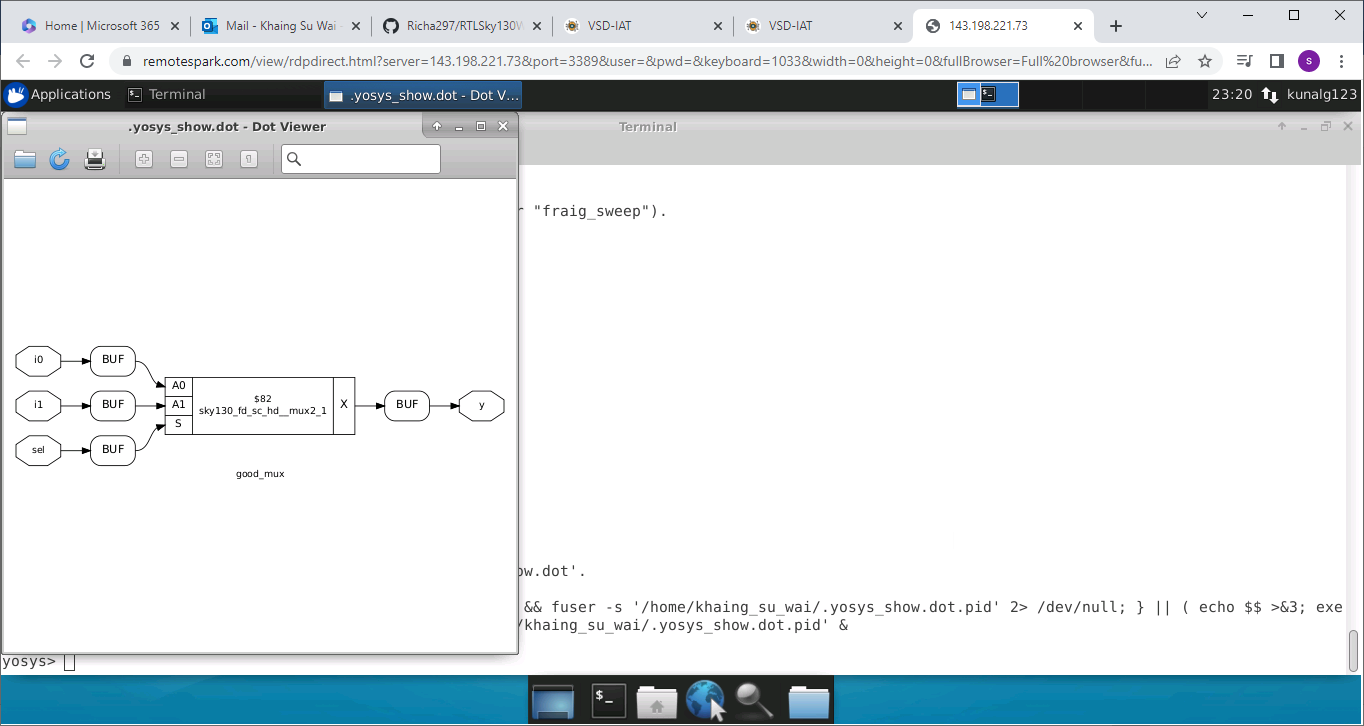
Pic 10

After writing Commands to obtain a synthesized implementation of good\_mux design



Pic 11

Pic 12

Pic 13

Pic 14

write\_verilog -noattr good\_mux\_netlist.v

!vim good mux\_netlist.v

